

Electronic Filing System (EFS) Data
Electronic Patent Application Submission
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EFS ID: 11575
Application ID: 09681776
Title of Invention: Diagonal Wiring Architecture for
Integrated Circuits
First Named Inventor: Steven Teig
Domestic/Foreign Application: Domestic Application
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TRANSMITTAL FORM

Electronic Version 1.0.2

Stylesheet Version: 1.0

Attorney Docket Number: SPLX.P0060

Diagonal Wiring Architecture for Integrated Circuits

First Named Inventor: Mr. Steven Teig

SUBMITTED BY

Name: Mr. Dag Johansen
Registration Number: 36172
Electronic Signature Mark: /Dag/ Date Signed: 20010603

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Attached Files:

bibd-transmittal	EFSapds.xml
specification	P0004EFS.xml
fee-transmittal	EFSfee.xml

Comments:

FEE TRANSMITTAL

Electronic Version 1.0.4

Stylesheet Version: 1.0

Patent fees are subject to annual revisions on or about October 1st of each year.

Large Entity

TOTAL FEES AUTHORIZED: \$ 1516

BASIC FILING FEE

Fee Description	Fee Code	Fee Paid
Utility Filing Fee	101	\$ 710

Subtotal For Basic Filing Fee: \$ 710

EXTRA CLAIM FEES

	Fee Code	Fee	Extra Claims	Fee Paid
Total Claims: 47	103	\$ 18	27	\$ 486
Independent Claims: 7	102	\$ 80	4	\$ 320

Subtotal For Extra Claims Fees: \$ 806